

ABSTRACT OF THE DISCLOSURE

An image pickup area including a plurality of pixels arranged in a two-dimensional matrix pattern, each pixel being an amplified MOS image sensor, is divided into a block A including odd-numbered columns of pixels and a block B including even-numbered columns of pixels. A horizontal signal line and an output amplifier are provided for each of the block A and the block B. Signal voltages on vertical signal lines are temporarily stored in a line memory. In the normal mode, the signal voltages of two pixels adjacent to each other in the horizontal direction are supplied from the line memory respectively to the output amplifiers. In the correction mode, a switch is closed to connect the horizontal signal lines with each other so that the signal voltage of the same pixel is supplied to the output amplifiers.